Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	10	("6792601" "6499063" "6405246" "6026428" "6178440").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 09:52
L2	19	((avoid minimiz$6) with (monitor$3 lock$3) with (deadlock$3 contention contend$3)) same ((access$3 acquir$3 obtain$3) with (common shar$3) with (resource processor cpu object disk$3 memor$3 queu$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:05
L3	9	((prevent$3 avoid minimiz$6) with (monitor$3 lock$3) with (starvation$2 deadlock$3 contention contend$3)) and (access$3 with (common shar$3) same (resource processor cpu object disk$3 memor$3 queu$3) same (acquir$3 obtain$3) same (process thread$3 task$3) same (lock$3) same (wait$3 queu$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:05
L4	25	(prevent$3 avoid minimiz$6) with (deadlock$3) with (thread$3 process task$3 request$3) with access$3 with (resource object)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:06
L5	23	(detect$3) same (deadlock$3 quiescen$2) same (throw$3 return$3 rais$3) same (exception)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:13
L6	0	l4 and l5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:24
L7	0	l2 and l5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:14
L8	0	l4 and 718/104.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:28

L9	3	14 and 718/100.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:28
L10	2	14 and 718/102.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:28
L11	2	14 and 710/200.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:28
L12	0	14 and 707/8.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:28
L13	2	13 and 718/104.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:26
L14	0	13 and 718/100.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:26
L15	1	13 and 707/8.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:26
L16	1	13 and 710/200.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:26
L17	1	13 and 707/8.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:26

L18	0	l2 and 718/104.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:27
L19	0	l2 and 718/100.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:27
L20	0	l2 and 718/102.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:27
L21	9	l2 and 710/200.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:27
L22	1	l2 and 707/8.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:28
L23	0	l5 and 718/104.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:28
L24	1	l5 and 718/100.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:28
L25	0	l5 and 718/102.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:28
L26	0	l5 and 710/200.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/30 10:28

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Inventor Name Search Result

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Last Name = HUTCHISON
First Name = GORDON

Application#	Patent#	Status	Date Filed	Title	Inventor Name
11133429	Not Issued	020	05/19/2005	METHOD AND ARRANGEMENT FOR FONT BUILDING	HUTCHISON, GORDON
10947809	Not Issued	020	09/23/2004	VALIDATING A VARIABLE DATA ITEM IN A SOFTWARE ROUTINE	HUTCHISON, GORDON D.
10814736	Not Issued	030	03/31/2004	MEMORY ALLOCATION	HUTCHISON, GORDON DOUGLAS
10155300	Not Issued	071	05/24/2002	PRIORITY INVERSION IN COMPUTER SYSTEM SUPPORTING MULTIPLE PROCESSES	HUTCHISON, GORDON DOUGLAS
10068337	Not Issued	071	02/07/2002	METHOD AND SYSTEM FOR DEADLOCK DETECTION AND AVOIDANCE	HUTCHISON, GORDON DOUGLAS
09885291	Not Issued	161	06/20/2001	ELECTRICAL SOCKET MEANS AND ASSOCIATED COMPONENTS	HUTCHISON, GORDON
09474608	Not Issued	080	12/29/1999	WEB PAGE DOWNLOADING	HUTCHISON, GORDON DOUGLAS
09389125	6389592	150	09/02/1999	METHOD FOR DEPLOYMENT OF INCREMENTAL VERSIONS OF APPLICATIONS	HUTCHISON, GORDON DOUGLAS
09344799	6499063	150	06/25/1999	CLIENT/SERVER COMPUTING FOR TRANSACTION PROCESSING WITH SUPERIOR COORDINATOR OPTIMIZATION	HUTCHISON, GORDON DOUGLAS
09344794	6457041	150	06/25/1999	CLIENT/ SERVER TRANSACTION DATA PROCESSING SYSTEM WITH OPTIMUM SELECTION OF LAST AGENT	HUTCHISON, GORDON DOUGLAS
09339378	6542922	150	06/25/1999	CLIENT/SERVER TRANSACTION DATA PROCESSING SYSTEM WITH AUTOMATIC DISTRIBUTED COORDINATOR SET UP INTO A LINEAR CHAIN FOR USE OF LINEAR COMMIT OTPIMIZATION	HUTCHISON, GORDON DOUGLAS

09252321	6405246	150	02/18/1999	AUTOMATIC AND DYNAMIC SOFTWARE CODE MANAGEMENT	HUTCHISON, GORDON DOUGLAS
08910723	6026428	150	08/13/1997	OBJECT ORIENTED THREAD CONTEXT MANAGER, METHOD AND COMPUTER PROGRAM PRODUCT FOR OBJECT ORIENTED THREAD CONTEXT MANAGEMENT	HUTCHISON, GORDON DOUGLAS
08910656	5893912	150	08/13/1997	THREAD CONTEXT MANAGER FOR RELATIONAL DATABASES, METHOD AND COMPUTER PROGRAM PRODUCT FOR IMPLEMENTING THREAD CONTEXT MANAGEMENT FOR RELATIONAL DATABASES	HUTCHISON, GORDON DOUGLAS
08887800	6178440	150	07/03/1997	DISTRIBUTED TRANSACTION PROCESSING SYSTEM IMPLEMENTING CONCURRENCY CONTROL WITHIN THE OBJECT REQUEST BROKER AND LOCKING ALL SERVER OBJECTS INVOLVED IN A TRANSACTION AT ITS START	HUTCHISON, GORDON DOUGLAS

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Last Name = PEACOCK
First Name = BRIAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10155300	Not Issued	071	05/24/2002	PRIORITY INVERSION IN COMPUTER SYSTEM SUPPORTING MULTIPLE PROCESSES	PEACOCK, BRIAN DAVID
10068337	Not Issued	071	02/07/2002	METHOD AND SYSTEM FOR DEADLOCK DETECTION AND AVOIDANCE	PEACOCK, BRIAN DAVID
09574137	6792601	150	05/18/2000	MULTIPLE MODE OBJECT LOCKING METHOD AND SYSTEM	PEACOCK, BRIAN DAVID

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41 Client-server computing in mobile environments
Jin Jing, Abdelsalam Sumi Helal, Ahmed Elmagarmid
June 1999   **ACM Computing Surveys (CSUR)**, Volume 31 Issue 2

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Recent advances in wireless data networking and portable information appliances have engendered a new
regardless of their physical location or movement behavior. In the meantime, research addressing inform
way ...

Keywords: application adaptation, cache invalidation, caching, client/server, data dissemination, disconr

42 Summary of the sigmetrics symposium on parallel and distributed processing
Jeffrey K. Hillingsworth, Barton P. Miller
March 1999 **ACM SIGMETRICS Performance Evaluation Review**, Volume 26 Issue 4

Full text available: 📄 pdf(1.17 MB)   Additional Information: full citation, index terms

43 NSF workshop on industrial/academic cooperation in database systems
Mike Carey, Len Seligman
March 1999 **ACM SIGMOD Record**, Volume 28 Issue 1

Full text available: 📄 pdf(1.96 MB)   Additional Information: full citation, index terms

44 A task- and data-parallel programming language based on shared objects
Saniya Ben Hassen, Henri E. Bal, Ceriel J. H. Jacobs
November 1998   **ACM Transactions on Programming Languages and Systems (TOPLAS)**, v

Full text available: 📄 pdf(434.44 KB)   Additional Information: full

Many programming languages support either task parallelism, but few languages provide a uniform frame
integrates task and data parallelism using shared objects. Shared objects may be stored on one processo
processes remotely a ...

Keywords: data parallelism, shared objects, task parallelism

45 Vclusters: a flexible, fine-grained object clustering mechanism

Mark L. Mcauliffe, Michael J. Carey, Marvin H. Solomon
October 1998   **ACM SIGPLAN Notices , Proceedings of the 13th ACM SIGPLAN conferenc**

Full text available: pdf(2.07 MB)   Additional Information: full

We consider the problem of delivering an effective fine-grained clustering tool to implementors and users concentrates on clustering *policies* (deciding which objects should be near each other). Existing on-line clu particularl ...

46 Concurrency and distribution in object-oriented programming
Jean-Pierre Briot, Rachid Guerraoui, Klaus-Peter Lohr
September 1998   **ACM Computing Surveys (CSUR)**, Volume 30 Issue 3

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This paper aims at discussing and classifying the various ways in which the object paradigm is used in con library approach applies object-oriented concepts, as they are, to structure concurrent and distributed sys

Keywords: concurrency, distribution, integration, libraries, message passing, object, reflection

47 Active messages: a mechanism for integrating communication and computation
Thorsten von Eicken, David E. Culler, Seth Copen Goldstein, Klaus Erik Schauser
August 1998 **25 years of the international symposia on Computer architecture (selected papers)**

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48 The NYU ultracomputer—designing a MIMD, shared-memory parallel machine
Allan Gottlieb, Ralph Grishman, Clyde P. Kruskal, Kevin P. McAuliffe, Larry Rudolph, Marc Snir
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49 Tempest and typhoon: user-level shared memory
Steven K. Reinhardt, James R. Larus, David A. Wood
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Full text available: pdf(1.57 MB)   Additional Information: full citation, references, index terms

50 Evaluation of hardware write propagation support for next-generation shared virtual memory cluster
Angelos Bilas, Liviu Iftode, Jaswinder Pal Singh
July 1998   **Proceedings of the 12th international conference on Supercomputing**

Full text available: pdf(1.28 MB)   Additional Information: full citation, references, citings, index terms

51 Guidance for the use of the Ada programming language in high integrity systems
B. A. Wichmann
July 1998   **ACM SIGAda Ada Letters**, Volume XVIII Issue 4

Full text available: pdf(2.93 MB)   Additional Information: full

This paper is the current result of a study by the ISO HRG Rapporteur group which is being circulated for substantial e-mail comments are: Praful V Bhansali (Boeing, USA), Alan Burns (University of York, UK), B (DGEI/INSA, France), George Roma ...

52 Performance modeling of multiprocessor implementations of protocols
Mats Björkman, Per Gunningberg

June 1998   **IEEE/ACM Transactions on Networking (TON)**, Volume 6 Issue 3

Full text available: pdf(285.80 KB)   Additional Information: full citation, references, citings, index terms, review

Keywords: multiprocessor, parallel communication protocols, performance modeling, queueing network

53 The design, implementation, and evaluation of Jade

Martin C. Rinard, Monica S. Lam

May 1998   **ACM Transactions on Programming Languages and Systems (TOPLAS)**, v

Full text available: pdf(576.88 KB)   Additional Information: full

Jade is a portable, implicitly parallel language designed for exploiting task-level concurrency.Jade progran program access data. The Jade implementation uses this data access information to automatically extract original serial program ...

Keywords: parallel computing, parallel programming languages

54 Tolerating latency in multiprocessors through compiler-inserted prefetching

Todd C. Mowry

February 1998   **ACM Transactions on Computer Systems (TOCS)**, Volume 16 Issue 1

Full text available: pdf(410.70 KB)   Additional Information: full

The large latency of memory accesses in large-scale shared-memory multiprocessors is a key obstacle to instructions to move data close to the processor before the data are actually needed. To minimize the bur

Keywords: compiler optimization, prefetching

55 Performance evaluation of the Orca shared-object system

Henri E. Bal, Raoul Bhoedjang, Rutger Hofman, Ceriel Jacobs, Koen Langendoen, Tim Rühl, M. Frans Kaasho

February 1998   **ACM Transactions on Computer Systems (TOCS)**, Volume 16 Issue 1

Full text available: pdf(179.39 KB)   Additional Information: full

Orca is a portable, object-based distributed shared memory (DSM) system. This article studies and evalua coherence protocol (based on write-updates with function shipping), the totally ordered group communica applications ill ...

Keywords: distributed shared memory, parallel processing, portability

56 Adaptive, fine-grained sharing in a client-server OODBMS: a callback-based approach

Markos Zaharioudakis, Michael J. Carey, Michael J. Franklin

December 1997   **ACM Transactions on Database Systems (TODS)**, Volume 22 Issue 4

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For reasons of simplicity and communication efficiency, a number of existing object-oriented database ma their efficiency, page servers are often criticized as being too retrictive when it comes to concurrency, as server context. Sev ...

Keywords: cache coherency, cache consistency, client-server databased, fine-grained sharing, object-ori

57 Tera hardware-software cooperation

Gail Alverson, Preston Briggs, Susan Coatney, Simon Kahan, Richard Korry

November 1997   **Proceedings of the 1997 ACM/IEEE conference on Supercomputing (CDR**

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The development of Tera's MTA system was unusual. It respected the need for fast hardware and large st clean machine model enabling calculated compiler optimizations and easy programming; and the need for molded the MTA architecture. ...

58 Fast detection of communication patterns in distributed executions
Thomas Kunz, Michiel F. H. Seuren
November 1997   **Proceedings of the 1997 conference of the Centre for Advanced Studies (**

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Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time event tracer developed at the University of Waterloo. However, these diagrams are often very complex ar trivial commun ...

59 CPU reservations and time constraints: efficient, predictable scheduling of independent activities
Michael B. Jones, Daniela Roşu, Marcel-Cătălin Roşu
October 1997   **ACM SIGOPS Operating Systems Review , Proceedings of the sixteenth ACM sym**

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60 Application performance and flexibility on exokernel systems
M. Frans Kaashoek, Dawson R. Engler, Gregory R. Ganger, Hector M. Briceño, Russell Hunt, David Mazières,
October 1997   **ACM SIGOPS Operating Systems Review , Proceedings of the sixteenth ACM syn**

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25 Speculative synchronization: applying thread-level speculation to explicitly parallel applications
José F. Martínez, Josep Torrellas
October 2002   **Proceedings of the 10th international conference on Architectural suppo**

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Barriers, locks, and flags are synchronizing operations widely used programmers and parallelizing compile assumptions about the program, or merely for code simplicity.We propose *Speculative Synchronization,* w active barriers, busy ...

26 The state of the art in locally distributed Web-server systems
Valeria Cardellini, Emiliano Casalicchio, Michele Colajanni, Philip S. Yu
June 2002   **ACM Computing Surveys (CSUR)**, Volume 34 Issue 2

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The overall increase in traffic on the World Wide Web is augmenting user-perceived response times from the needed scalability to handle large traffic volumes and to match rapid and dramatic changes in the nur architectures. This article w ...

Keywords: Client/server, World Wide Web, cluster-based architectures, dispatching algorithms, distribut

27 Hardware Support: A system-on-a-chip lock cache with task preemption support
Bilge Saglam Akgul, Jaehwan Lee, Vincent John Mooney
November 2001   **Proceedings of the 2001 international conference on Compilers, architec**

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Intertask/interprocess synchronization overheads may be significant in a multiprocessor-shared memory consumption in the system. It has been shown that a hardware solution brings a much better performanc hardware mechanism which resolves the Critical ...

Keywords: RTOS, SoC, lock synchronization, multi-processor synchronization, preemption, shared memc

28 Papers: Off the wall: Focus plus context screens: combining display technology with visualization te
Patrick Baudisch, Nathaniel Good, Paul Stewart
November 2001   **Proceedings of the 14th annual ACM symposium on User interface softw**

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Computer users working with large visual documents, such as large layouts, blueprints, or maps perform currently have to choose between using a sufficiently large screen or applying appropriate visualization te allow the user to m ...

Keywords: Display, fisheye view, focus plus context screen, mixed resolution, overview plus detail, videc

29 Portable resource control in Java
Walter Binder, Jane G. Hulaas, Alex Villazón
October 2001   **ACM SIGPLAN Notices , Proceedings of the 16th ACM SIGPLAN conferenc**

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Preventing abusive resource consumption is indispensable for all kinds of systems that execute untrusted some support for resource control must be available: accounting and limiting the usage of physical resour envisaged here, even th ...

Keywords: Java, bytecode rewriting, micro-kernels, mobile object systems, resource control, security

30
Real-time convergence of Ada and Java™

Ben Brosgol, Brian Dobbing
September 2001   **ACM SIGAda Ada Letters , Proceedings of the 2001 annual ACM SIGAda il**

Full text available: pdf(191.98 KB)   Additional Information: full

Two independent recent efforts have defined extensions to the Java platform that intend to satisfy real-tir
Annex, and argues that their convergence with Ada95 may serve to complement rather than compete wit

Keywords: Ada, Java, Real-Time, asynchrony, garbage collection, scheduling, threads

31 Session 1: Decentralized ad-hoc groupware API and framework for mobile collaboration
Dominik Buszko, Wei-Hsing (Dan) Lee, Abdelsalam (Sumi) Helal
September 2001   **Proceedings of the 2001 International ACM SIGGROUP Conference on Su**

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We describe a mobile collaborative system designed for wireless, ad-hoc collaboration. In recent years, m
devices have become more pervasive. From smart phones, and personal digital assistants (PDAs) running
provided communication capabili ...

Keywords: ad-hoc collaboration, decentralized groupware, mobile and wireless collaboration

32 Computing curricula 2001
September 2001   **Journal on Educational Resources in Computing (JERIC)**

Full text available: pdf(613.63 KB)   html(2.78 KB)   Additional Information: full citation, references, citings, index terms

33 WireGL: a scalable graphics system for clusters
Greg Humphreys, Matthew Eldridge, Ian Buck, Gordan Stoll, Matthew Everett, Pat Hanrahan
August 2001   **Proceedings of the 28th annual conference on Computer graphics and inl**

Full text available: pdf(333.39 KB)   Additional Information: full

We describe WireGL, a system for scalable interactive rendering on a cluster of workstations. WireGL prov
parallel interface. We also describe techniques for reassembling an output image from a set of tiles distrit
displ ...

Keywords: cluster rendering, parallel rendering, remote graphics, scalable rendering, tiled displays, virtu

34 External memory algorithms and data structures: dealing with **massive data**
Jeffrey Scott Vitter
June 2001   **ACM Computing Surveys (CSUR)**, Volume 33 Issue 2

Full text available: pdf(828.46 KB)   Additional Information: full

Data sets in large applications are often too massive to fit completely inside the computers internal memc
a major performance bottleneck. In this article we survey the state of the art in the design and analysis o
. consider a varie ...

Keywords: B-tree, I/O, batched, block, disk, dynamic, extendible hashing, external memory, hierarchica

35 Lightweight kernel/user communication for real-time and multimedia applications
Christian Poellabauer, Arsten Schwan, Richard West
January 2001   **Proceedings of the 11th international workshop on Network and operatii**

Full text available: pdf(240.90 KB)   Additional Information: full

Operating system enhancements to support real-time and multimedia appl ications often include specializ
flexible interactions of such extensions with user-level functionality, we have developed ECalls, a lightwei

frequ ...

36 Cycles to recycle: garbage collection to the IA-64
Richard L. Hudson, J. Elliot Moss, Sreenivas Subramoney, Weldon Washburn
October 2000   **ACM SIGPLAN Notices , Proceedings of the 2nd international symposium**

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The IA-64, Intel's 64-bit instruction set architecture, exhibits a number of interesting architectural feature
by describing how one may exploit features of the IA-64. Along the way, we record some previously unpu
would simplify it ...

37 Session summaries from the 17th symposium on operating systems principle (SOSP'99)
Jay Lepreau, Eric Eide
April 2000 **ACM SIGOPS Operating Systems Review**, Volume 34 Issue 2

Full text available: pdf(3.15 MB)   Additional Information: full citation, index terms

38 Progress-based regulation of low-importance processes
John R. Douceur, William J. Bolosky
December 1999   **ACM SIGOPS Operating Systems Review , Proceedings of the seventeent**

Full text available: pdf(1.53 MB)   Additional Information: full

MS Manners is a mechanism that employs progress-based regulation to prevent resource contention with
contention that degrades the performance of a high-importance process will also retard the progress of th
resource contention from a drop in the p ...

Keywords: process priority, progress-based feedback, symmetric resource contention

39 Borrowed-virtual-time (BVT) scheduling: supporting latency-sensitive threads in a general-purpose
Kenneth J. Duda, David R. Cheriton
December 1999   **ACM SIGOPS Operating Systems Review , Proceedings of the seventeent**

Full text available: pdf(1.81 MB)   Additional Information: full

Systems need to run a larger and more diverse set of applications, from real-time to interactive to batch,
complex real-time paradigms, limiting their applicability to general-purpose systems.In this paper, we pre
sharin ...

40 The holodeck ray cache: an interactive rendering system for global illumination in nondiffuse enviro
Gregory Ward, Maryann Simmons
October 1999   **ACM Transactions on Graphics (TOG)**, Volume 18 Issue 4

Full text available: pdf(935.74 KB)   Additional Information: full

We present a new method for rendering complex environments using interactive, progressive, view-indep
interactive walk-throughs of nondiffuse environments with full global illumination. Ray sample density var
disk and ...

Keywords: illumination, image reconstruction, mesh generation, ray tracing, rendering system, virtual re

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